

IoT PLL

FEATURES

- > Optimized for very low power, running completely from core power supply.
- > Supports 32KHz reference clocks.
- Extremely wide range of operation with multiplication factors over 8,000.
- Small area, delivered as a single hard macro with guardrings and isolation.
- > Flexible and highly programmable.
- Ideal for low power and cost sensitive applications such as IoT wearables and remote sensors.
- TSMC, GLOBALFOUNDRIES and UMC processes from 65nm to 28nm.

DELIVERABLES

- GDSII and LVS Spice netlist, behavioral, synthesis and LEF models, and extensive user documentation.
- Integration support to ensure a successful tape out (included in standard design license fee).

SPECIFICATIONS PART NO. TCI-TN40LP-IOTPLL

- Divided reference frequency range
- /1 output frequency range
- Reference divider values
- Feedback divider values
- Output divider values
- /1 output multiples of div. reference
- Bandwidth adjustment div. range
- Feedback signal delay (max))
- Output duty cycle (nom, tol)
- Static phase error (max)
- Period jitter (P-P) (max)
- Long-term jitter (RMS) (max)
- Power dissipation (nom)
- Reset pulse width (min)
- Reset /1 output frequency range
- Lock time (min allowed)
- /1 output freq. overshoot (max)
- Area (including isolation) (max)
- Number of PLL supply pkg. pins
- Low freq. supply noise est. (P-P) (max)
- Low freq. sub. noise est. (P-P) (max)
- Ref. input jitter (long-term, P-P) (max)
- Reference H/L pulse width (min)
- Process technology
- Supply voltage (VDD, VDDA) (nom, tol)
- Junction temperature (nom, min, max)

+/-2.5% output cycle n/a 45uW @ 30MHz (/1 output) 5us 20MHz - 200MHz 500 div. reference cycles

30KHz - 250MHz

30MHz - 250MHz

1, 2-16 (even only)

n/a (FB internal)

50%, +/-5% (/1), +/-2% (/N)

1 - 64

1-8192

1-8192

1-8192

n/a

500 div. reference cycl 40%/50% ~0.063mm^2

1 VDDA, 1 VSSA (preferred) 10% VDDA 10% VDDA 2% div. reference cycle 330ps

TSMC CLN40LP 40nm 1.1V, +/-10% 70C, -40C, 125C

* Jitter numbers are worst-case estimates with 10% VDDA supply and substrate noise—actual results will be better.

